

General Description

The 060N10 uses advanced technology and design to provide excellent RDS(ON). This device is ideal for PWM, load switching and general purpose applications.

Product Summary

BVDSS	RDS(ON)	ID
100V	7mΩ	80A

Applications

- DC-DC Converters
- Power switching application

TO-252/251 Pin Configuration**Features**

- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

Absolute Maximum Ratings

Type	Package	Marking
CMD060N10	TO-252	CMD060N10
CMU060N10	TO-251	CMU060N10

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current	80	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current	60	A
I_{DM}	Pulsed Drain Current	320	A
EAS	Single Pulse Avalanche Energy ¹	612	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	100	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (PCB mount)	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction -Case	---	1.5	°C/W

Electrical Characteristics ($T_j=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	100	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=28\text{A}$	---	---	7	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_j=25^\circ\text{C}$	---	---	1	uA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=20\text{A}$	---	20	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2	---	Ω
Q_g	Total Gate Charge		---	45	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=50\text{V}$, $I_{\text{D}}=27.5\text{A}$	---	12	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	---	9	---	
$T_{\text{d(on)}}$	Turn-On Delay Time		---	23	---	ns
T_r	Rise Time	$V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=27.5\text{A}$	---	9	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$R_G=4.7\Omega$, $R_L=1.82\Omega$	---	63	---	
T_f	Fall Time		---	19	---	
C_{iss}	Input Capacitance		---	3500	---	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1250	---	
C_{rss}	Reverse Transfer Capacitance		---	60	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	80	A
I_{SM}	Pulsed Source Current		---	---	320	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=28\text{A}$, $T_j=25^\circ\text{C}$	---	---	1.2	V

Notes:

1.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=40\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $I_{\text{D}}=49.5\text{A}$

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

