

# CMD65R290/CMU65R290

650V, 0.27Ω typ., 13A N-Channel Super Junction Power MOSFET

## General Description

The 65R290 is power MOSFET using Cmos's advanced super junction technology that can realize very low on resistance and gate charge. It will provide much high efficiency by using optimized charge coupling technology. These user friendly devices give an advantage of low EMI to designers as well as low switching loss.

## Product Summary

<b>BVDSS</b>	<b>RDS(on) max.</b>	<b>ID</b>
650V	0.3Ω	13A

## Applications

- Charger
- Adaptor
- Power Supply

## TO-252/TO-251 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	650	V
V <sub>GS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current (Note 1)	13	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current	8	A
I <sub>DM</sub>	Pulsed Drain Current (Note 2)	52	A
EAS	Single Pulse Avalanche Energy (Note 3)	453	mJ
P <sub>D</sub> @T <sub>c</sub> =25°C	Total Power Dissipation	132	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Rating	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient	127	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case	0.95	°C/W

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Electrical Characteristics ( $T_j=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	650	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=6\text{A}$	---	0.27	0.3	Ω
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	2	---	4	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=650\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_j=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=650\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_j=150^\circ\text{C}$	---	5	---	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=6\text{A}$	---	7	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	9	---	Ω
$Q_g$	Total Gate Charge	$I_D=7.5\text{A}$	---	24	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	5	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	10	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DS}}=400\text{V}$ $V_{\text{GS}}=10\text{V}$	---	14	---	ns
$T_r$	Rise Time		---	24	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	97	---	
$T_f$	Fall Time		---	22	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=100\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	660	---	pF
$C_{\text{oss}}$	Output Capacitance		---	40	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	2.3	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	13	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	52	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=12\text{A}$ , $T_j=25^\circ\text{C}$	---	0.88	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$di/dt = 100\text{A}/\mu\text{s}$	---	250	---	ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$V_{\text{DS}}=100\text{V}$ , $I_{\text{SD}}=7.5\text{A}$	---	2.94	---	μC

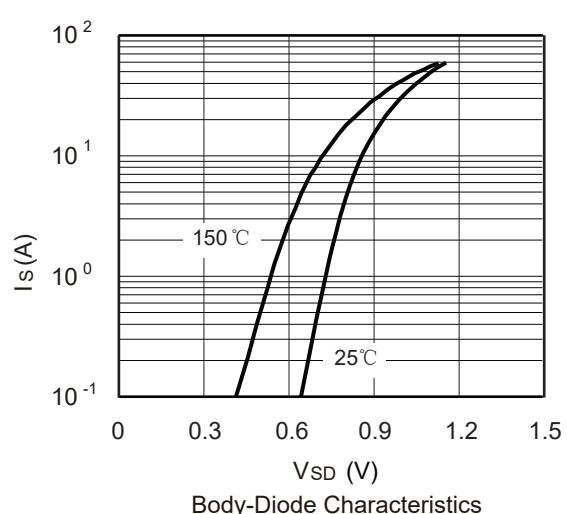
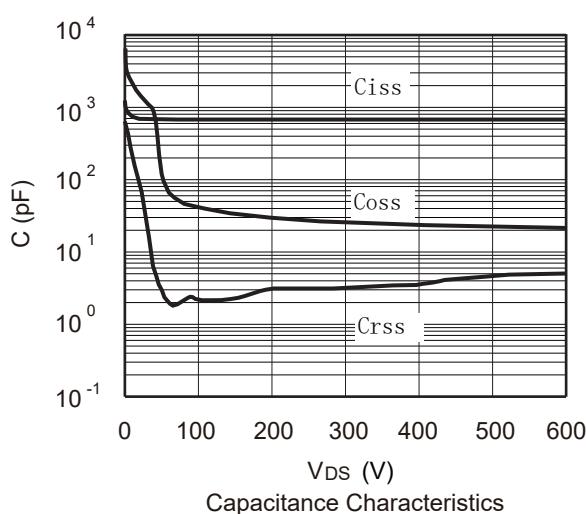
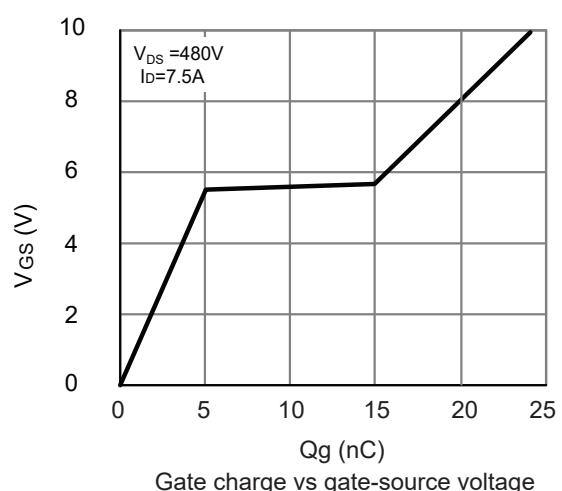
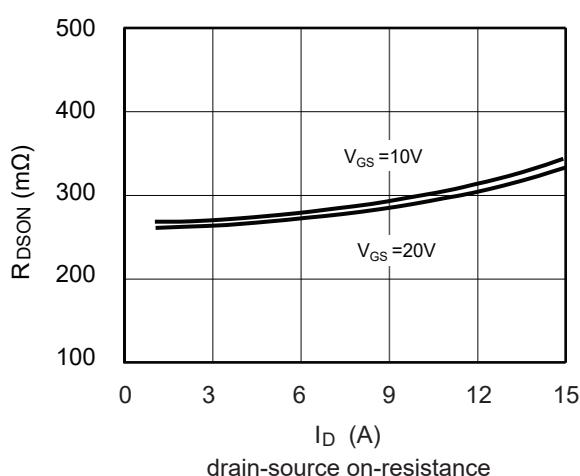
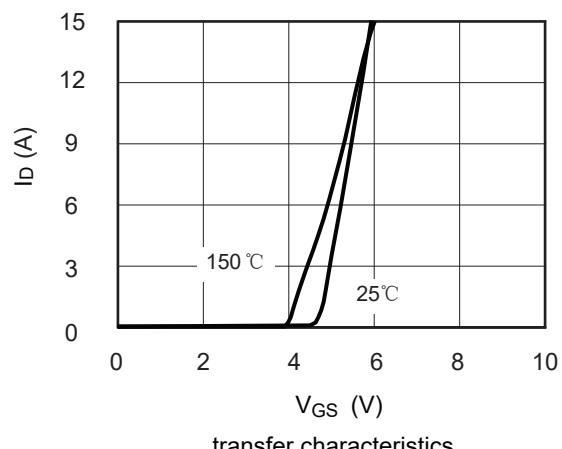
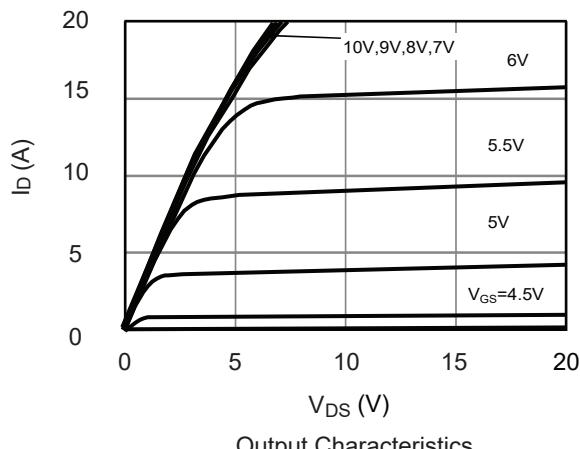
Notes:

- 1.Limited by  $T_j,\text{max}$ . Maximum Duty Cycle D = 0.50
- 2.Pulse width tp limited by  $T_j,\text{max}$ .
- 3.The EAS data shows Max. rating .The test condition is  $V_{\text{DS}}=80\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=30\text{mH}$ ,  $I_{\text{AS}}=5.5\text{A}$ .

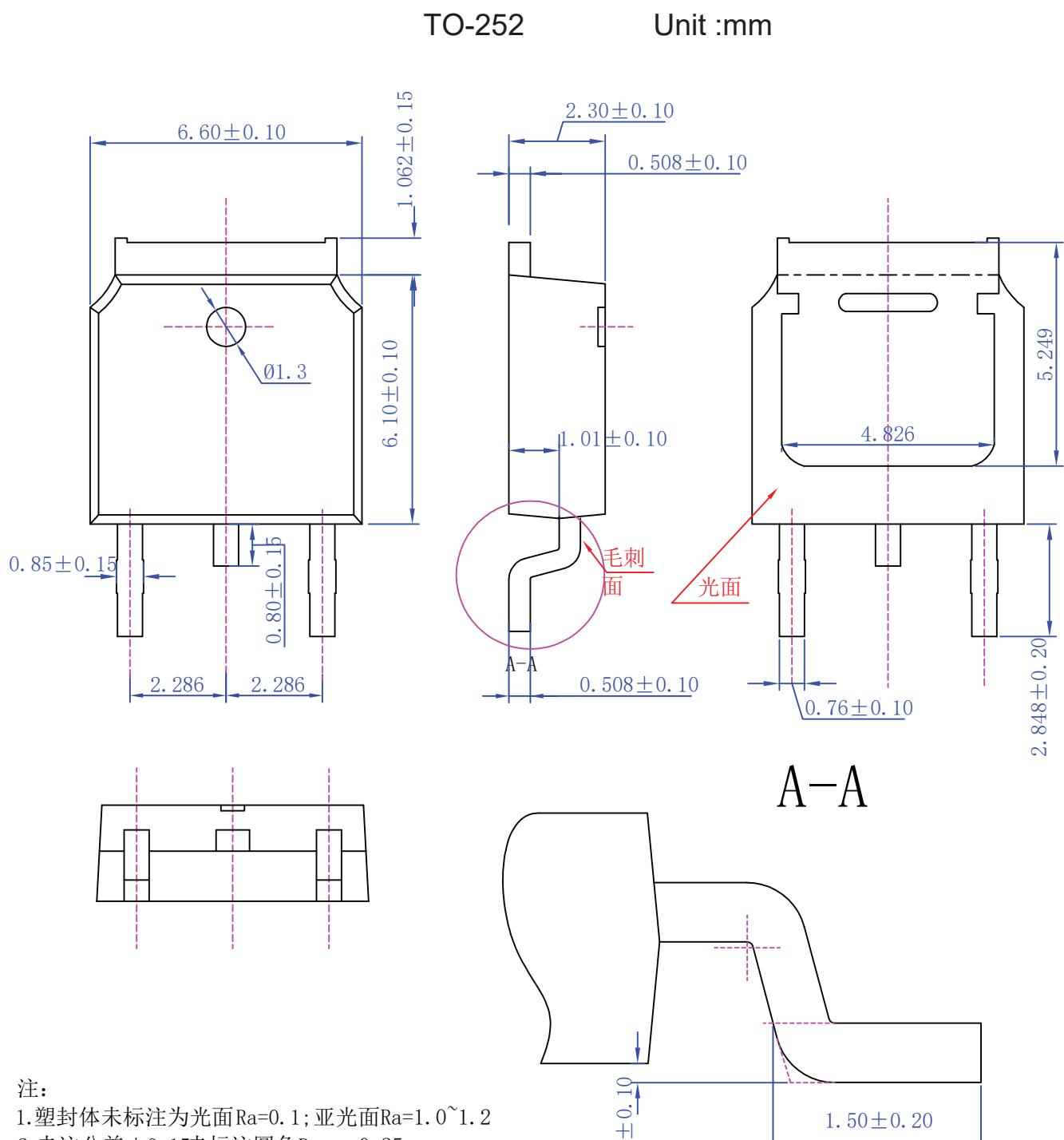
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 Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

### Typical Characteristics



## Package Dimension



注:

1. 塑封体未标注为光面  $R_a=0.1$ ; 亚光面  $R_a=1.0 \sim 1.2$
2. 未注公差  $\pm 0.15$  未标注圆角  $R_{max}=0.25$
3. 塑封体无缺损、缩孔、裂纹、气泡等不良缺陷
4. 标注单位 mm
5. 顶针孔不允许凸出塑封体表面

## Package Dimension

TO-251A

Unit :mm

