

40V N-Channel MOSFET

General Description

This N-Channel MOSFET has been produced using advanced trench technology to deliver low RDS(on) and optimized BVDSS capability to offer superior performance benefit in the application

Features

- Max $r_{DS(on)} = 15.5 \text{m}\Omega$ at $V_{GS} = 10 \text{V}$
- Max $r_{DS(on)} = 21m\Omega$ at $V_{GS} = 4.5V$
- Fast Switching
- RoHS Compliant

Product Summary

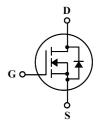
BVDSS	RDSON	ID
40V	15.5mΩ	50A

Applications

- Inverters
- Power Supplies

TO-252 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	40	V	
V_{GS}	Gate-Source Voltage	±20	V	
I _D @T _C =25℃	Continuous Drain Current 50			
I _{DM}	Pulsed Drain Current	150	А	
E _{AS}	Drain-Source Avalanche Energy ¹	100	mJ	
P _D @T _C =25℃	Total Power Dissipation 45		W	
T _{STG}	Storage Temperature Range -55 to 150		$^{\circ}$	
T _J	Operating Junction Temperature Range -55 to 150		$^{\circ}$	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{0JA}	Thermal Resistance Junction-ambient		40	°C/W
R _{θJC}	Thermal Resistance Junction-case		2.8	°C/W



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Electrical Characteristics ($T_J=25^{\circ}$ C), unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40			V
В ,	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =18A			15.5	mΩ
R _{DS(ON)}		V_{GS} =4.5V , I_D =15A			21	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1		3	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =32V , V _{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance ²	V _{DS} =10V, I _D =10A		10		S
Qg	Total Gate Charge	I _D =25A		18		
Q _{gs}	Gate-Source Charge	V _{DS} =20V		3		nC
Q_{gd}	Gate-Drain Charge	V _{GS} =4.5V		5		
T _{d(on)}	Turn-On Delay Time	V _{DS} =20V		8		
Tr	Rise Time	I _D =25A		15		ns
T _{d(off)}	Turn-Off Delay Time	R _{GEN} =6Ω		32		115
T _f	Fall Time	V _{GS} =10 V		7		
C _{iss}	Input Capacitance			1400		
Coss	Output Capacitance	V_{DS} =20V , V_{GS} =0V , f=1MHz		200		pF
C _{rss}	Reverse Transfer Capacitance			90		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
trr	Reverse Recovery Time	I _E =25A		15		ns
Qrr	Reverse Recovery Charge	di/dt=100A/μs		30		nC
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1.8 A			1.2	V

Notes:

1.Starting T_J = 25 $^{\circ}$ C, L = 0.5mH, ID =20 A, V DD = 40 V, VGS = 10 V.

2.Pulse Test: Pulse Width < 300µs, Duty cycle < 2.0%.

This product has been designed and qualified for the counsumer market.

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