

### General Description

The CMH20N50 uses advanced planar stripe DMOS technology and design to provide excellent RDS(ON).

These devices are well suited for high efficient switched mode power supplies and active power factor correction.

### Features

- 100% avalanche tested
- Fast Switching
- Improved dv/dt capability
- RoHS Compliant

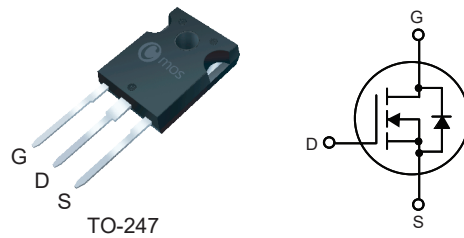
### Product Summary

BVDSS	RDSON	ID
500V	0.26 Ω	20A

### Applications

- Switching regulators
- UPS (Uninterruptible Power Supply)
- DC-DC converters

### TO247 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	500	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current	20	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current	12	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	60	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	860	mJ
$I_{AS}$	Avalanche Current	20	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	280	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	40	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-case	---	0.44	$^\circ\text{C/W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	500	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =250uA	---	0.5	---	V/°C
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	---	---	0.26	Ω
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2	---	4	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V	---	---	1	uA
		V <sub>DS</sub> =400V, V <sub>GS</sub> =0V, TC=125°C	---	---	10	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance <sup>3</sup>	V <sub>DS</sub> =40V, I <sub>D</sub> =10A	---	24	---	S
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =20A V <sub>DS</sub> =400V V <sub>GS</sub> = 10V (Note 3, 4)	---	52	69	nC
Q <sub>gs</sub>	Gate-Source Charge		---	18	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	26	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =250V I <sub>D</sub> =20A R <sub>G</sub> =25Ω (Note 3, 4)	---	88	---	ns
T <sub>r</sub>	Rise Time		---	270	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	105	---	
T <sub>f</sub>	Fall Time		---	117	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	---	4000	---	pF
C <sub>oss</sub>	Output Capacitance		---	380	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	35	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	20	A
I <sub>SM</sub>	Pulsed Source Current		---	---	60	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =20 A, T <sub>J</sub> =25°C	---	---	1.4	V

Note :

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.L = 4.1mH, I<sub>AS</sub> = 20A, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C
- 3.Pulse Test: Pulse width≤300μs, Duty Cycle≤2%
- 4.Essentially Independent of Operating Temperature Typical Characteristics

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Cmos assumes no liability for customers' product design or applications.  
Cmos reserves the right to improve product design, functions and reliability without notice.

