

General Description

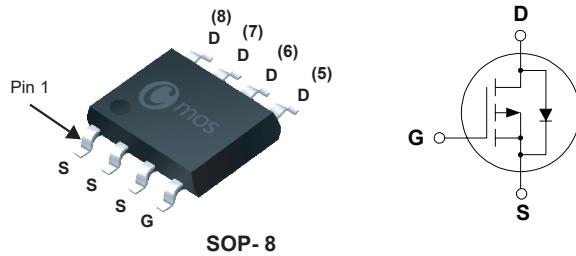
The CMS4013 uses advanced trench technology to provide excellent RDS(ON). This device is ideal for load switch and battery protection applications.

Product Summary

BVDSS	RDS(ON)	ID
-40V	18mΩ	-13A

Applications

- Load switch
- Power management
- Battery protection

SOP-8 Pin Configuration**Features**

- P-Channel MOSFET
- Low ON-resistance
- Surface Mount Package
- RoHS Compliant

Type	Package	Marking
CMS4013	SOP- 8	CMS4013

Absolute Maximum Ratings (TA=25 °C Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25°C	Continuous Drain Current	-13	A
I _D @T _A =70 °C	Continuous Drain Current	-9	A
I _{DM}	Pulsed Drain Current	-39	A
EAS	Single Pulse Avalanche Energy ¹	173	mJ
P _D @T _A =25°C	Total Power Dissipation	2.7	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance, Junction-to-Ambient	---	47	°C/W

P-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-40	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_D=-10\text{A}$	---	16	18	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_D=-8\text{A}$	---	22	25	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-1	---	-3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-32\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}} = \pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_D=-10\text{A}$	---	21	---	S
Q_g	Total Gate Charge	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_D=-7\text{A}$	---	23	---	nC
Q_{gs}	Gate-Source Charge		---	10	---	
Q_{gd}	Gate-Drain Charge		---	8	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-20\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D = 3.5\text{A}$	---	44	---	ns
T_r	Rise Time		---	48	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	150	---	
T_f	Fall Time		---	20	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2800	---	pF
C_{oss}	Output Capacitance		---	270	---	
C_{rss}	Reverse Transfer Capacitance		---	180	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{SD}}=-8\text{A}$	---	-0.81	-1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=0.3\text{mH}$, $I_{\text{AS}}=-34\text{A}$.

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P-Channel Enhancement Mode MOSFET

Typical Characteristics

